| Ref<br># | Hits | Search Query  | DBs                              | Default<br>Operat<br>or | Plural<br>s | Time Stamp          |
|----------|------|---|----------------------------------|-------------------------|-------------|---------------------|
| L1       | 0    | isulat\$5 near4 dielectric near4<br>((two "2" pair plurality<br>multipl\$5 couple) near3<br>conduct\$5) near5 transverse<br>near4 capacit\$6  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |
| L2       | 0    | (isulat\$5 near4 dielectric) and ((two "2" pair plurality multipl\$5 couple) near3 conduct\$5) and (transverse near4 capacit\$6)              | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |
| L3       | 1    | (isulat\$5 near4 dielectric) and ((two "2" pair plurality multipl\$5 couple) near3 conduct\$5) and (capacit\$6)                               | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |
| L4       | 3    | (isulat\$5 near4 dielectric) and ((two "2" pair plurality multipl\$5 couple) near3 conduct\$5)  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:29 |
| L5       | 8680 | (capacit\$5 near3 switch).clm.  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:29 |
| L6       | 0    | 5 and (isulat\$5 near4 dielectric)  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:29 |
| L7       | 0    | 5 and (isulat\$5 near7 dielectric)  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:29 |
| L8       | 0    | 5 and (isulat\$5 and dielectric)  | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |
| L9       | 431  | 5 and (insulat\$5 and dielectric)   | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |
| L10      |      | insulat\$5 near4 dielectric<br>near4 ((two "2" pair plurality<br>multipl\$5 couple) near3<br>conduct\$5) near5 transverse<br>near4 capacit\$6 | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR                      | OFF         | 2005/10/31<br>07:30 |

| L11 | 1.1201 | (insulat\$5 near4 dielectric)<br>and ((two "2" pair plurality<br>multipl\$5 couple) near3<br>conduct\$5) and (capacit\$6)                     | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR | OFF | 2005/10/31<br>07:30 |
|-----|--------|---|----------------------------------|----|-----|---------------------|
| L12 | 47     | (insulat\$5 near4 dielectric)<br>and ((two "2" pair plurality<br>multipl\$5 couple) near3<br>conduct\$5) and (transverse<br>near4 capacit\$6) | US-PGPU<br>B; USPAT;<br>EPO; JPO | OR | OFF | 2005/10/31<br>07:31 |